

Michael C. Hollinger

Location: Austin, TX
Interested in: Permanent-Hire Positions

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Senior Technical Staff Member • Chief Engineer • Senior Developer with 11 years of experience building, leading, and working in high performance cross-functional teams • International Assignee • Master Inventor

PROFESSIONAL EXPERIENCE

07/16 – Present	IBM Systems Group, OpenPOWER Solutions Architecture	<i>Senior Technical Staff Member & Chief Engineer, Software-Defined Storage Systems</i>	Austin, TX
<ul style="list-style-type: none">• Cross-functional technical leader, OpenPOWER Systems Software-Defined Storage Platform, focused on Swift and CEPH.			
04/14 – 07/16	IBM Systems & Technology Group, Systems Software Development Organization	<i>Chief SW Engineer for CAPI Flash Accelerators, Team Lead, and Platform Integration Lead</i>	Austin, TX
<ul style="list-style-type: none">• Led software design and integration across multidisciplinary teams for IBM's first CAPI accelerator, and follow-on CAPI NVMe accelerator.• Primary Author and Editor, "IBM Data Engine for NoSQL – Integrated Flash Edition Solution Reference" (http://ibm.co/2eMZzoX).			
04/12 – 04/14	IBM Systems & Technology Group, Taiwan Systems Technology Lab	<i>International Assignee & Software Architect, Seeding a new Global Market Team</i>	Taipei, TW
<ul style="list-style-type: none">• Led and Mentored Taiwan-based team, growing to 29 members over 2011 to 2013, hiring staff to design, integrate, and test Linux applications for IBM Power Systems embedded firmware. Established software best practices and trained IBM processes. Represented team globally.• Refactored specific components / build processes to improve testability and long-term maintainability of Taiwan-owned software artifacts; designed and implemented continuous integration build pipelines across multiple components.			
07/10 – 04/12	IBM Systems & Technology Group, Firmware Development Organization	<i>Advisory Software Engineer & Team Lead for a 10-person Java Development Team</i>	Austin, TX
<ul style="list-style-type: none">• Refined requirements from stakeholders, translating into a multi-year iterative project plan, built on the Agile / Scrum development model. Led the team as both a scrum master and kernel team lead.• Established a Dev-Ops software development pipeline built on Git / Gerrit for source control, Maven for build management, TestNG for test case design, Jenkins for Continuous Delivery, and Sonar for quality measurement.			
12/08 – 01/10	IBM Systems & Technology Group, Power Systems UNIX Servers	<i>System Functional Owner, Power 750 / Power 755 Enterprise-Class Server</i>	Austin, TX
<ul style="list-style-type: none">• Led cross-component integration for the IBM Power Systems 750 / 755, the first shipping server for the POWER7 processor, delivering releases on-time, and meeting manufacturing, system test and customer commitments.• Achieved platform stability several months ahead of schedule, working closely with the Chief Engineer, Release Architect, and Project Manager.			
06/05 – 07/10	IBM Systems & Technology Group, EnergyScale™ Technology	<i>Embedded Systems Developer, Power-Aware Firmware</i>	Austin, TX
<ul style="list-style-type: none">• Designed, wrote and tested embedded software for performance-aware power management across POWER6 and POWER7 servers.• Productized algorithms from IBM Research, leading to several patents.			

EDUCATION

08/04 – 05/05

University of Oklahoma, Norman OK – Accelerated M.S. E.E. / Computer Engr.

- 3.5 / 4.0 GPA w/ Thesis Defended: *Server-Side Software for Multimodal User Interactions*

08/00 – 05/04

University of Oklahoma, Norman, OK – B.S. Computer Engineering

- 3.8 / 4.0 GPA, Summa Cum Laude, including 16 hours of honors credit
- '03-'05 President of Eta Kappa Nu Electrical & Computer Engineering Honor Society, '04 Outstanding Senior in Computer Engr., 2004-05 Co-Chair IEEE OU Tech. Society
- 2004 IBM Extreme Blue Intern, Austin, TX

HONORS / ACTIVITIES / AWARDS

- 2012, 2016 IBM Master Inventor
- 2011 IBM Major Outstanding Technical Achievement Award, Delivery of POWER7
- 2010 IBM "Bravo" Award, IBM Power 750 Server
- 33 Patents Filed, 21 Patents Granted
- 2008 IBM Outstanding Innovation Award, EnergyScale™ Technology
- Co-Chair, Out@IBM GLBT Diversity Network
- Board of Visitors, Univ. of Oklahoma School of ECE
- Student Intermediate-level Spoken Mandarin

SKILLS

- Embedded SW Architecture
- C, C++, Java7 Development
- Agile Team Leadership
- Dev-Ops w/ Jenkins, Ansible
- Linux User-space Development
- Client-Facing Presentations
- FPGA Accelerator Offload
- Gerrit / Git Source Control
- HW/FW Interface Design